

05/04/98
1c588 U.S. PRO

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

APPLICANT(S): Brian R. Baird et al.
SERIAL NO.: 08/740,248
FILING DATE: October 25, 1996
TITLE: Debug and Video Queue for Multi-Processor Chip
EXAMINER: Unknown
GROUP ART UNIT: 2304
ATTY. DKT. NO.: 3315

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CERTIFICATE OF MAILING

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Dated: 4/27/98

By: [Signature]
Greg T. Sueoka, Reg. No.: 33,800

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REQUEST FOR CORRECTED FILING RECEIPT

SIR:

Enclosed is a copy of the Official Filing Receipt. It contains the following error:

1. The title is erroneously stated as "Debug and Video Queue for **Multi-Procerssor** Chip". The correct title is "Debug and Video Queue for **Multi-Processor Chip**", as evidenced by the Patent Application Cover Sheet, a copy of which is enclosed.

Please issue a corrected Filing Receipt rectifying this error.

☒ The correction is not due to any error by the Applicant and therefore no fee is due.

☐ Since at least one of the corrections is due to Applicant's error, payment in the amount of \$25, pursuant to 37 CFR § 1.19(h), is enclosed.

1c588 U.S. PTO
05/04/98

Respectfully submitted,
BRIAN R. BAIRD ET AL.

Dated: _____

4/27/98

By: _____

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FILING RECEIPT



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
08/740,248	10/25/96	2304	\$385.00	RM-60	7	20	3

STUART T AUVINEN
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Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Application Processing Division's Customer Correction Branch within 10 days of receipt. Please provide a copy of the Filing Receipt with the changes noted thereon.

Applicant(s)

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FOREIGN FILING LICENSE GRANTED 11/22/96

* SMALL ENTITY *

TITLE

DEBUG AND VIDEO QUEUE FOR MULTI-PROCESSOR CHIP

PRELIMINARY CLASS: 395

PATENT APPLICATION**Debug and Video Queue for Multi-Processor Chip****INVENTORS:**

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